Parallel writing by local oxidation nanolithography with submicrometer resolution

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We demonstrate that the process of local oxidation of surfaces by atomic force microscopy (AFM) can be upscaled in a straightforward way by using a solid support with multiple protrusions as the cathode electrode. A metallized digital video disk DVD polymeric support has been used as a stamp to generate an array of features of variable length and 100 nm in width in a silicon surface covering a 5×6 mm² region. The parallel patterning process involves the formation of as many liquid bridges as there are protrusions in the stamp. The growth rate of the parallel local oxides is slightly smaller than the one obtained by AFM experiments. Nonetheless, results from AFM local oxidation experiments can be readily extended to parallel oxidation which in turn opens the possibility of patterning centimeter-square regions with 10 nm motives.

Local oxidation or nano-oxidation of metallic, organic, and semiconducting films by a conductive probe of an atomic force microscope (AFM) is emerging as a reliable and versatile method to fabricate nanometer-scale structures and devices. Nano-oxidation has been applied to yield patterns made of dots and lines of controlled size and positions that have been used to demonstrate bit writing with areal densities of about 1 terabit/in.², to generate sets of interdigitated lines with a periodicity of 13 nm, to fabricate submicrometer gratings, or optical waveguides with subwavelength cross sections. Local oxidation nanolithography has also been applied to fabricate several electronic devices with critical features in the nanometer range, such as single-electron transistors, superconducting interference devices, or antidot lattices. Local oxidation has also been proven useful to seed defects acting as nuclei for crystallization or anchoring sites for receptors of biomolecules, or to modify useful to seed defects acting as nuclei for crystallization or anchoring sites for receptors of biomolecules, to modify self-assembled monolayers, or to fabricate templates to orient growth of molecular thin films. Moreover, negative features, such as nanosized holes and trenches, can be achieved by anisotropic etching of the oxide layer.

Local oxidation is compatible with ambient operation, microelectronic processing, and does not require masks or lift-off. In terms of resolution and writing speed, it is comparable to state-of-the-art electron-beam lithography, with the advantage of being more sustainable in terms of cost and being based on a widespread microscopy technique. The shortcomings arise from the strictly serial nature of the process that hinders its technological implications and by the limited area in which fabrication can be performed. The patterned area is limited by the maximum scan size of the piezoelectric scanner, usually in the 10⁻³ mm² range. It would be therefore extremely desirable to transfer the same process developed for AFM into a parallel fabrication method capable of similar performance in terms of positioning and feature definition, but covering centimeter-square regions. Although parallel local oxidation was demonstrated by using an array of 50×1 parallel cantilevers, current technical limitations in the fabrication and operation of arrays of parallel cantilevers have slowed down this approach.

Nano-oxidation has strong similarities with conventional anodic oxidation. The AFM tip is used as a cathode and the water meniscus formed between tip and surface is the source of the oxyanions species. The strong localization of the electrical field lines within the meniscus gives rise to a nanometer-sized oxide dot. The physics and chemistry of individual local oxidation processes are rather general and do not imply the use of an AFM tip as a cathode to perform the oxidation. Consequently, a setup can be envisioned in which the cathode is an array of metallic protrusions grown, deposited, or attached onto a solid support.

In this letter, we propose and demonstrate a method to pattern centimeter-square areas with submicrometer motives by using a parallel local oxidation scheme. We show that local oxidation can be effectively used with stamps brought into intimate contact with a silicon oxide surface. The stamps are metallic films grown onto a recorded digital video disk (DVD) polymeric support. By applying voltage pulses to the junction nanostructures of silicon oxide are grown onto the substrate over several square millimeters. The oxide structures replicate the position and periodicity of the protrusions of the stamp; however, their lateral width is about 100 nm, smaller than the stamp features. Although the use of a stamp bears some resemblance to new lithographic methods for patterning soft materials, our approach is a resistless patterning process.

The experiment, schematically shown in Fig. 1, was performed using a home-built press able to hold substrates up to 6×5 cm². The plate holding the stamp slides along three cylinders that maintain it parallel to the substrate. A set of calibrated springs holds the plates against each other. Once
in contact, a load force can be applied by tightening a micrometric screw. Here, we have applied load forces of approximately 2.5 kg/cm². The press is inserted in a chamber with controlled relative humidity by fluxing water-saturated nitrogen.

The stamp consists of a 1 cm² piece of polycarbonate cut out of a recorded DVD, with an effective contact area of 5 × 6 mm², and coated with 100 nm Au film evaporated in high vacuum. Figure 2 shows the topography of the stamp’s surface. Au film covers conformally the relieves of the DVD substrate, with 90-nm-high and 260 nm-wide (full width at half-maximum) hillocks. Protrusions with variable lengths ranging from 350 to 1500 nm are present on the surface.

The substrate to be modified is a silicon (111) wafer covered with a native oxide layer of about 1 nm. To transfer the motives from the stamp to the substrate a 15 or 20 V bias voltage for a time ranging between 20 and 180 s is applied to silicon. Those parameters are comparable to the parameters used to perform local oxidation in contact and noncontact modes.¹⁹

Figures 3(a) and 3(b) show the result over an area which is representative of the whole modified surface. The structures are 95±7 nm wide, with a variable length and a height of 0.7±0.2 nm with respect to the substrate baseline. The cross section in Fig. 3(c) along the line marked in (b) shows their height. These structures can be found all over the region in intimate contact with the stamp, and their characteristic sizes are rather homogeneous across the whole area. The distance between parallel lines is approximately 740 nm, that matches perfectly that of the original DVD. No effect of increasing the load force is observed on the sample in terms of modification, displacement, or damage to the fabricated structures. Furthermore, the surface has been immersed in a...
10% solution of hydrofluoric acid for 2 min. The acid etches selectively silicon oxide versus silicon, leaving \( \approx 1 \) nm-deep trenches in correspondence of the fabricated structures. This is an indication that the process relevant to the creation of nanostructures is anodic oxidation through the formation of as many water bridges as there are protrusions in the stamp. Here, the formation of liquid bridges is driven by both the mechanical contact between the protrusions and the sample surface and the field-effect condensation of water molecules in the proximity of the protrusions.\(^1\)\(^2\)\(^1\) The electrical field would allow the formation of water meniscus even in the absence of mechanical contact.

Figure 3(b) shows a detailed view of the fabricated structures. The cross-section in Fig. 3(c) along the line marked in (b) reveals the height of the fabricated oxide structures. A statistics on the structures yields a width of 95 \( \pm 7 \) nm, a mean height of 0.7 nm and a roughness of less than 0.2 nm on top of the structures.

We have performed some preliminary experiments to determine the oxidation kinetics of parallel oxidation. Figure 4 shows the effect of the duration of the oxidation process on the height of the nanostructures. The oxide growth rate is slower than that of an AFM tip. The smaller electrical field makes the oxidation diffusion barriers higher. Second, it has been observed that the kinetics of local oxidation depends on the AFM operating mode (contact or noncontact). Specifically, contact oxidation occurs at a lower rate with respect to noncontact oxidation.\(^1\)\(^9\) Third, the measurements of Fig. 4 are average values estimated over a statistical sample of more than 100 oxide structures made by independent protrusions, whereas in AFM there is only one protrusion (the tip).

Although in our experiment the stamp is pressed against the surface, it is reasonable to assume that only a fraction of the protrusions are in mechanical contact with the substrate. Nonetheless all the protrusions are transferred to the substrate. Figure 4 shows that the standard deviation ranges from 25% to 50% of the mean height, which suggests, indeed, a nonuniform protrusion–sample separation. The presence of a water meniscus bridging the protrusions and the substrate, implies that the voltage drops mostly within the native silicon oxide layer. This is because of the difference between water and silicon dioxide dielectric constants (80 versus 3.9). This may explain the ability to transfer all the protrusions despite of the variability of separation.

In conclusion, the process of local oxidation of surfaces by AFM can be upscaled by using a multiple source of protrusions. This has allowed the patterning of a \( 5 \times 6 \) mm\(^2 \) region with submicrometer motives in 50 ms. The patterning requires the formation of multiple and independent nanometer-sized water bridges. The body of AFM oxidation knowledge together with the use of a suitable design of the protrusions and the fine control of the distance between stamp and substrate with spacers\(^1\)\(^8\)\(^2\)\(^2\)\(^2\)\(^2\) allows us to predict that the fabrication of features with sub-10 nm resolution will be feasible with the same overall method.\(^2\)\(^3\)

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\(^23\) As we were completing this work, we have become aware of a work reporting the transfer of micrometric patterns on a rigid metallic object to a highly ordered organosilane monolayer on silicon upon the application of a voltage between the object and the silicon substrate; S. Hoeppener, R. Maoz, and J. Sagiv, Nano Lett. 3, 761 (2003).