Proposal for a phase-coherent thermoelectric transistor
F. Giazotto, J. W. A. Robinson, J. S. Moodera, and F. S. Bergeret

Citation: Applied Physics Letters 105, 062602 (2014); doi: 10.1063/1.4893443

View online: http://dx.doi.org/10.1063/1.4893443

View Table of Contents: http://scitation.aip.org/content/aip/journal/apl/105/6?ver=pdfcov

Published by the AIP Publishing

Articles you may be interested in
Single flux transistor: The controllable interplay of coherent quantum phase slip and flux quantization

Enhancement of thermoelectric efficiency in a double-quantum-dot molecular junction

On-chip hot spot cooling using silicon thermoelectric microcoolers
J. Appl. Phys. 102, 034503 (2007); 10.1063/1.2761839

Multistage thermoelectric microcoolers
J. Appl. Phys. 95, 8226 (2004); 10.1063/1.1737473

Ultralow dissipation Josephson transistor
Proposal for a phase-coherent thermoelectric transistor

F. Giazotto,1,a) J. W. A. Robinson,2,b) J. S. Moodera,3 and F. S. Bergeret4,5,c)
1NEST, Scuola Normale Superiore, I-56127 Pisa, Italy
2Department of Materials Science and Metallurgy, University of Cambridge, 27 Charles Babbage Road, Cambridge CB3 0FS, United Kingdom
3Department of Physics and Francis Bitter Magnet Lab, Massachusetts Institute of Technology, Cambridge, Massachusetts 02139, USA
4Centro de Física de Materiales (CFM-MPC), Centro Mixto CSIC-UPV/EHU, Manuel de Lardizabal 4, E-20018 San Sebastián, Spain
5Donostia International Physics Center (DIPC), Manuel de Lardizabal 5, E-20018 San Sebastián, Spain

(Received 19 June 2014; accepted 26 July 2014; published online 15 August 2014)

Identifying materials and devices which offer efficient thermoelectric effects at low temperature is a major obstacle for the development of thermal management strategies for low-temperature electronic systems. Superconductors cannot offer a solution since their near perfect electron-hole symmetry leads to a negligible thermoelectric response; however, here we demonstrate theoretically a superconducting thermoelectric transistor which offers unparalleled figures of merit of up to ~45 and Seebeck coefficients as large as a few mV/K at sub-Kelvin temperatures. The device is also phase-tunable meaning its thermoelectric response for power generation can be precisely controlled with a small magnetic field. Our concept is based on a superconductor-normal metal-superconductor interferometer in which the normal metal weak-link is tunnel coupled to a ferromagnetic insulator and a Zeeman split superconductor. Upon application of an external magnetic flux, the interferometer enables phase-coherent manipulation of thermoelectric properties whilst offering efficiencies which approach the Carnot limit. © 2014 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4893443]

It is known that electron-hole symmetry breaking is essential for a material to possess a finite thermoelectric figure of merit.1,2 In principle, conventional superconductors have a near perfect symmetric spectrum and therefore are not suitable for thermoelectric devices. However, if the density of states is spin-split by a Zeeman field a superconductor-ferromagnet hybrid device can provide a thermoelectric effect3–5 with a figure of merit close to 1. Here, we propose a multifunctional phase-coherent superconducting transistor in which the thermoelectric efficiency is tunable through an externally applied magnetic flux. A giant Seebeck coefficient of several mV/K and a figure of merit close to ~45 is predicted for realistic materials parameters and materials combinations.

The phase-coherent thermoelectric transistor is based on two building blocks. The first one is sketched in Fig. 1(a) and consists of a superconducting film (SR) tunnel-coupled to a normal metal (N) by a ferromagnetic insulator (FI). The latter induces an exchange field (h) in SR which leads to a Zeeman split superconducting DoS. The spectrum for spin-up (↑) and spin-down (↓) electrons is given by

\[ \nu_{SR}(E) = E/2 \nu_{BCS}(E \pm h), \]

where \( \nu_{BCS}(E) = |\text{Re}[(E + i \Gamma)/\sqrt{(E + i \Gamma)^2 + \Delta_0^2(T, h)}]| \) is the conventional Bardeen-Cooper-Schrieffer DoS in a superconductor, \( E \) is the energy, \( \Delta_0 \) is the order parameter, and \( \Gamma \) accounts for broadening. Due to the presence of the spin-splitting field, \( \Delta_0 \) depends on temperature (T) and h. While the total DoS of \( S_F \), \( \nu_{SF}(E) = \nu_{SR}(E) + \nu_{SN}(E) \), is electron-hole symmetric [Fig. 1(b)], the spin-dependent \( \nu_{SF}(E) \) components are no longer even functions of the energy. This means that electron-hole imbalance can, in principle, be achieved using a spin-filter contact with a normal metal. This would yield a finite thermoelectric effect4,5 in the N/FI/SR heterostructure shown in Fig. 1(a). In addition to providing a Zeeman splitting, the FI also serves as a spin-filter between both metals. Therefore, if one applies a temperature difference \( \delta T \) between N and SR, a finite charge current (I) will flow through the junction due to the thermoelectric effect, which is given by

\[ I = P e R_T \int \frac{E \nu_N(E) [\nu_{SN}(E) - \nu_{SF}(E)]}{4k_B T \cosh^2 \left( \frac{E}{2k_B T} \right)} \text{d}E, \]

where \( P \) is the spin polarization due to the spin-filtering action of the FI, \( T \) is the average temperature of N and SR, \( k_B \) is the Boltzmann constant, \( R_T \) is the junction resistance, and \( \nu_N(E) \) is the DoS of the N layer.

Our proposal is based on the following observation: if one tunes the DoS in N, in such a way that \( \nu_N(E) \) is enhanced at those energies where the coherent peaks of the Zeeman-split superconductor occur, then \( z \) should drastically increase in accordance to Eq. (2). Ideally, one would search for a material with a tunable DoS and this can be realized with our second building block: a proximity superconducting quantum interference device (SQUID), as sketched in Fig. 1(c). This consists of a superconducting loop interrupted by a normal metal wire. The contacts between N and Sr are assumed to be transparent thus allowing superconducting correlations to be induced in the N region. The latter manifest as opening a...
FIG. 1. The phase-coherent thermoelectric transistor. (a) Sequence of stacked metallic layers consisting of a superconductor (S₀) tunnel-coupled to a normal metal (N) through a ferromagnetic insulator (FI). This implements the thermoelectric building block of the transistor. The FI layer induces an exchange field \( \Phi \) in the superconductor and simultaneously provides a spin-polarizing barrier of polarization \( P \). \( T \) and \( T + \delta T \) denote the temperatures of the N and S₀ layers, respectively. (b) Total density of states vs energy \( E \) in S₀ showing the exchange-field-induced spin splitting. In the present case, we set a finite \( h = 0.3 \Delta_0 \), where \( \Delta_0 \) is the order parameter. (c) A superconducting quantum interference device (SQUID) containing a superconductor-normal metal-superconductor (S₀ N S₁) proximity Josephson junction of length \( L \). The loop is pierced by an externally applied magnetic flux \( \Phi \) which allows the DoS in the N region to be varied, \( \phi \) is the quantum phase difference across the N wire. The proximity SQUID implements the phase-coherent element of the transistor. (d) Density of states \( \nu_\text{SR} \) in the N wire vs energy \( E \) calculated for a few values of the applied magnetic flux. \( \Phi_0 \) denotes the flux quantum. (e) Sketch of the resulting phase-coherent thermoelectric transistor obtained by joining the thermoelectric element in (a) with the S₀ N S₁ proximity SQUID shown in (c). In particular, the N wire is placed on top of the FI layer and is therefore tunnel-coupled to S₀ as in the scheme shown in panel (a). Under temperature bias, a thermovoltage \( V \) and a current \( I \) can develop across the transistor which can be finely tuned through the magnetic flux \( \Phi \) of the SQUID. \( S \) indicates the transistor Seebeck coefficient.

The efficiency of the thermoelectric transistor can be quantified by the usual dimensionless figure of merit \( ZT \) defined as

\[
ZT = \frac{S^2 \sigma T}{\kappa},
\]

where \( \kappa = \kappa - \alpha^2 / \sigma T \) is the thermal conductance at zero current and \( S = -\kappa / (\sigma T) \) is the Seebeck coefficient that is a measure for the thermopower [see Fig. 1(e)]. We stress that there is no theoretical limit for \( ZT \): if it approaches infinity the efficiency of the transistor would reach the Carnot limit. So far, only exceptionally good thermoelectric bulk materials are able to provide values of \( ZT \) slightly larger than 1 [see Fig. 4(a)]. By contrast, our thermoelectric transistor can result in \( ZT \) values as large as several tens thanks to the fine tuning offered via the phase. This is displayed in Fig. 2 where the flux dependence of \( ZT \) as a function of different parameters is shown. In Fig. 2(a), the temperature and splitting field are fixed at moderate values, 0.1Tc, and 0.2\( \Delta_0 \), respectively.\(^{14,16–18} \) \( T_c \approx (1.764 k_B)^{-1} \Delta_0 \) denotes the superconducting critical temperature, \( \Delta_0 \) is the zero-temperature, zero-exchange field energy gap, and for clarity we assume that both S₀ and S₁ have the same \( \Delta_0 \) value. Therefore, by tuning the flux through the loop the theoretical zero-temperature limit assuming no inelastic scattering processes for \( ZT = P^2 / (1 - P^2) \) (indicated by colored arrows corresponding to each \( P \) value)\(^4 \) can be approached without requiring ultra-low temperatures. It is also apparent that a
high barrier polarization is crucial in order to achieve large values of ZT. Good candidates for the tunneling barriers are europium chalcogenides (EuO and EuS) for which values of P ranging from 80% up to almost 100% have been reported.\textsuperscript{13–16,18–21} Also very high spin-filtering has been reported in GdN barriers\textsuperscript{22–24} with polarizations as large as 97% at 15 K (even larger values are expected as T decreases).

In Fig. 2(b), \( P \) is set to 98%, \( T = 0.1 T_c \), and ZT for different values of the induced Zeeman field \( h \) is plotted showing an interesting non-monotonic behavior. \( h \) cannot be externally tuned but can be partially controlled during growth of the FI/SR interface since it depends on the quality of the contact. Values from 0.1 meV up to few meV for the Zeeman splitting have been reported.\textsuperscript{16,21,25,26}

We emphasize that the amplitude of the thermoelectric effect also depends on the length of the N wire. We assumed that \( L \) is smaller than the superconducting coherence length [see Eq. (3)] or meaning \( \Delta_0 < \varepsilon_{TF} \), where \( \varepsilon_{TF} = hD/L^2 \) is the Thouless energy and \( D \) is the diffusion coefficient of the N wire. Although these two energies in the existing experiments on SQUIPTs are of same order,\textsuperscript{14,15} the observed modulation of the induced gap by the magnetic flux can be well described with Eq. (3).\textsuperscript{14} Instead of the N bridge, one can use a short constriction made with the same material of the loop. In such a case, one avoids the mismatch of the Fermi velocities at the interfaces, meaning the proximity effect between the bulky part of the loop and the constriction is increased.

Figure 2(c) shows the flux dependency of ZT for different values of the temperature, at \( P = 98\% \) and \( h = 0.2 \Delta_0 \). It is clear that the efficiency of the transistor decreases by increasing the temperature towards \( T_c \).

Figures 3(a) and 3(b) show the temperature dependence of ZT for different magnetic fluxes. ZT reaches a maximum at a finite \( T \) that corresponds to the temperature for which the difference between the gap induced in N and the gap in the SR electrode matches the value of the exchange field, i.e., when the coherent peaks in the DoS at the edges of the gaps [see Figs. 1(b) and 1(d)] coincide. The red dashed lines in Figs. 3(a) and 3(b) show the upper theoretical value of ZT at zero temperature.\textsuperscript{4} Our device, however, allows to approach this maximum value at finite \( T \) by tuning the magnetic flux. The full dependence of ZT on both \( T \) and \( h \), for \( P = 98\% \) and \( h = 0.2 \Delta_0 \) is shown by the color plot in Fig. 3(c). For \( T \approx 0.1 T_c \) and \( \Phi \approx 0.25 \Phi_0 \) or \( \Phi \approx 0.75 \Phi_0 \), the figure of merit can reach values close to \( P^2/(1 - P^2) \approx 25 \).

To place our device in the context of thermoelectrics, we show in Fig. 4(a) the state-of-the-art bulk materials with the highest ZT values. One of the most widely used material is Bi\(_2\)Te\(_3\),\textsuperscript{27,28} which at room temperature shows a ZT close to 1. All other materials show their highest values of ZT at high temperatures. By contrast, our transistor operates at low temperatures.
temperatures where it can reach values of $ZT$ which are larger by more than an order of magnitude. An appropriate candidate is EuO combined with superconducting Al where we expect a $ZT$ of $\sim 30-40$ for realistic values of the spin-filter efficiency. Other suitable Eu chalcogenides include EuS or EuSe. In the latter case, $P$ and $h$ can be tuned by an external magnetic field. An alternative to the chalcogenides is to use GdN with superconducting NbN. Its advantage is the higher critical temperature of NbN of $14\text{ K}$ and $h = 0.2 \Delta_0$. Furthermore, for both of them we set $\phi = 0.2 \Phi_0$. Right side: Figure of merit $ZT$ vs temperature possessed by several state-of-the-art commercial bulk thermoelectric materials. Dash-dotted line indicates $ZT = 1$. Absolute value of the Seebeck coefficient ($S$) achievable in the phase-coherent thermoelectric transistor vs $T$ calculated for a few selected values of the applied magnetic flux at $h = 0.2 \Delta_0$ and $P = 98\%$. Data shown in panel (a) are taken from Ref. 27.

The temperature-voltage conversion capability of the thermoelectric transistor can be quantified by the Seebeck coefficient $S$: its temperature dependence is shown in Fig. 4(b) for selected values of the applied magnetic flux. Here, we set $P = 98\%$ and $h = 0.2 \Delta_0$. The transistor is extremely sensitive at low temperatures and provides a sharp response even to tiny temperature gradients. In particular, Seebeck coefficients as large as a few mV/K can be achieved under optimal flux tuning conditions, which have to be compared to coefficients as large as a few hundreds of $\mu$V/K obtained in the abovementioned high-performance thermoelectric materials. These sizeable values of $S$ make our phase-coherent thermoelectric transistor an ideal candidate for the implementation of cryogenic power generators or ultrasensitive low-temperature general purpose thermometry as well as, for radiation sensors where heating of one of the superconductors forming the junction is achieved due to a coupling with radiation.

The work of F.G. has been partially funded by the European Research Council under the European Union’s Seventh Framework Programme (FP7/2007-2013)/ERC grant Agreement No. 615187-COMANCHE, and by the Marie Curie Initial Training Action (ITN) Q-NET 264034. J.W.A.R. acknowledges funding from the Royal Society (“Superconducting Spintronics”). J.W.A.R. and F.S.B. jointly acknowledge funding from the Leverhulme Trust through an International Network Grant (Grant No. IN-2013-033). J.S.M acknowledges funding from the National Science Foundation (Grant No. DMR 1207469) and the Office of Naval Research (Grant No. N00014-13-1-0301). The work of F.S.B. has been supported by the Spanish Ministry of Economy and Competitiveness under Project No. FIS2011-28851-C02-02.